Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1950	(365/226).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/08/11 09:13
L2	185	(level-shift or level-shifting or (level adj (shift or shifting)) or levelshift or levelshifting) adj (buffer or latch)	US-PGPUB; USPAT; USOCR	AND	ON	2005/08/11 09:34
L3	113	L2 and (semiconductor or semi-conductor or (semi adj conductor))	US-PGPUB; USPAT; USOCR	AND	ON	2005/08/11 09:36
L4	66	L3 and memory	US-PGPUB; USPAT; USOCR	AND	ON	2005/08/11 09:33
L5	46	L4 and microprocessor	US-PGPUB; USPAT; USOCR	AND	ON	2005/08/11 09:33
L7	53383	(semiconductor or semi-conductor or (semi adj conductor)) adj memory	US-PGPUB; USPAT; USOCR	AND	ON	2005/08/11 09:37
L8	1977	L7 and (voltage adj generator)	US-PGPUB; USPAT; USOCR	AND	ON	2005/08/11 09:38
L9	1121	365/189.11.ccls.	US-PGPUB; USPAT; USOCR	AND	ON	2005/08/11 09:42
L10	1495	365/189.07.ccls.	US-PGPUB; USPAT; USOCR	AND	ON	2005/08/11 09:43